



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY JAMSHEDPUR  
Jamshedpur – 831014, Jharkhand (State), India  
(An Institution of National Importance under MHRD, Government of India)

Ref. No. NITJSR/EEE/135/2018

Date: 07/03/2018

**Notice**

Mr. V V Rajagopal Peesapati, Roll no. 09RS/12, Reg. No – 2012RSEE001, Research Scholar of Department of Electrical & Electronics Engineering, is going to defend his Ph.D. Thesis on the topic “**Efficacy Evaluation of Indian Power Distribution Utility in a Deregulated Environment**” on 09.03.2018 (Friday) at 4.00 PM in the Room No: 203.

All the members of D.S.C, faculty members and research scholars are requested to be present during the Ph.D. Defence Viva – Voce examination.

**Members of D.S.C**

1. H.O.D.  
Department of Electrical & Electronics Engineering, NIT JSR
2. Dr. R. N. Mahanty, Professor  
Department of Electrical & Electronics Engineering, NIT JSR
3. Dr. D.A. Khan, Associate Professor  
Department of Electrical & Electronics Engineering, NIT JSR
4. Dr. U. K. Sinha, Associate Professor  
Department of Electrical & Electronics Engineering, NIT JSR
5. Dr. Niranjana Kumar  
Department of Electrical & Electronics Engineering, NIT JSR
6. Dr. V. K. Yadav  
Department of Electrical Engineering, Gautam Budha  
University

Chairman — Aman  
07-03-2018

Member — [Signature]

Member — [Signature]

Member — [Signature]

Supervisor & Convenor — Aman  
07-03-2018

Supervisor

Aman  
07.03.2018  
Head (EEED)

Copy to:

1. Director in favour of kind information
2. All Deans
3. Registrar
4. All HODs with request for circulation among the faculty members, research scholars and PG students
5. PI, Website for needful action