



राष्ट्रीय प्रौद्योगिकी संस्थान, जमशेदपुर
NATIONAL INSTITUTE OF TECHNOLOGY, JAMSHEDPUR
JAMSHEDPUR, (JHARKHAND)-831014
(An Institution of National importance under MHRD, Govt. of India, NEW DELHI)
Department of Electrical and Electronics Engineering

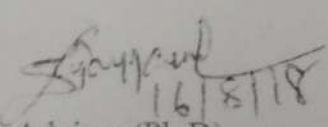
Ref. No. NIT JSR/DEE/Ph.D/2018/104

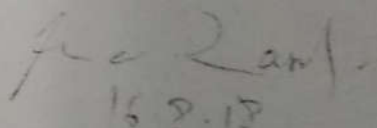
Date: 16-08-2018

NOTICE

Ms. Anumeha, (Roll No. 11/RS/12, Reg. No.2012RSEE002), Research Scholar, in the Department of Electrical and Electronics Engineering will defend her Ph.D. thesis titled "Gravitational Search Algorithm Implemented Optimal Economic Operation of Power System" on 23/08/2018 at 11.00 AM in Room no. 205, Department of Electrical and Electronics Engineering.

All the members of DSC, interested faculty members, research scholars, and PG students are cordially invited to attend the Ph.D. defence seminar.


Faculty Advisor (Ph.D)


16.8.18
H.O.D (EEED)

Copy to:

- The Director NIT Jamshedpur: for favour of kind information
- All Deans
- All H.O.Ds with a request to circulate the same among faculty members, Research Scholar and PG students of department
- The Registrar(Accounts & Finance)
- ✓ The PI, Website
- The Head, Electrical and Electronics Engineering for kind information and needful

B.S. Pal
21/8/18 at 3:56 pm